TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention

METHOD OF FABRICATING A CMOS DEVICE WITH DUAL METAL GATE ELECTRODES

Application Number : 10/8266

10/826665

Date : 2004-04-16

First Named Applicant: PARK CHANG SEO

Confirmation Number: 9608

Attorney Docket Number: 61472-0308425

- I hereby certify that the use of this system is for OFFICIAL correspondence between patent applicants or their representatives and the USPTO. Fraudulent or other use besides the filing of official correspondence by authorized parties is strictly prohibited, and subject to a fine and/or imprisonment under applicable law.
- I, the undersigned, certify that I have viewed a display of document(s) being electronically submitted to the United States Patent and Trademark Office, using either the USPTO provided style sheet or software, and that this is the document(s) I intend for initiation or further prosecution of a patent application noted in the submission. This document(s) will become part of the official electronic record at the USPTO.

Submitted By:	Elec. Sign.	Sign. Capacity
DAVID H. JAFFER	/David H. Jaffer/	Attorney
Registered Number: 32,243		

Documents being submitted:	Files	
us-ids	061472-0308425-usidst.xml	
	us-ids.dtd	
	us-ids.xsl	
Comments		